

Electronic Acknowledgement Receipt

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Application Number:	10044217
International Application Number:	
Confirmation Number:	9153
Title of Invention:	System and method for connecting a logic circuit simulation to a network
First Named Inventor/Applicant Name:	Robert M. Zeidman
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1		Resp_Final_OA-BNK-16302.pdf	123016 8d1ba38103feb5350dae59a413ab6b244e75bdb9	yes	17
	Multipart Description/PDF files in .zip description				
	Document Description		Start	End	
	Amendment Submitted/Entered with Filing of CPA/RCE		1	1	
	Claims		2	11	
	Applicant Arguments/Remarks Made in an Amendment		12	17	
Warnings:					
Information:					
2	Extension of Time	Ext_Time.pdf	64307 a7376160d24e99fd12ec6c46642360febbf62cfd	no	1
Warnings:					
Information:					
3	Request for Continued Examination (RCE)	RCE_Form.pdf	697485 3e0b84d476887fa8450061dd21b2878e3b605703	no	3
Warnings:					
Information:					
4	Application Data Sheet	ADS.pdf	1103996 5c6bcb90563392fe102fd44575c03a2a38198191	no	4
Warnings:					
Information:					
5	Fee Worksheet (PTO-875)	fee-info.pdf	32290 0f67b3920e00b676c372b7dac480d9d0a67c775c	no	2
Warnings:					
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Total Files Size (in bytes):			2021094		

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